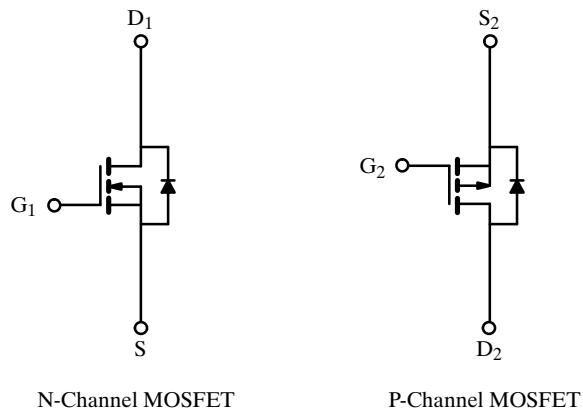
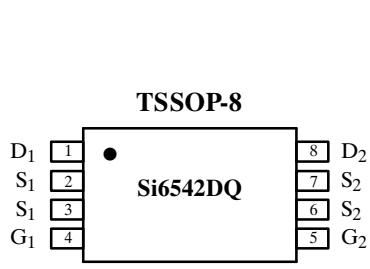


TEMIC

Siliconix

Si6542DQ**Dual Enhancement-Mode MOSFET (N- and P-Channel)****Product Summary**

	V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
N-Channel	20	0.09 @ $V_{GS} = 10$ V	± 2.5
		0.175 @ $V_{GS} = 4.5$ V	± 1.8
P-Channel	-20	0.17 @ $V_{GS} = -10$ V	± 1.9
		0.32 @ $V_{GS} = -4.5$ V	± 1.3

**Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)**

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V_{DS}	20	-20	V
Gate-Source Voltage	V_{GS}	± 20	± 20	
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	± 2.5	± 1.9	A
		± 2.0	± 1.5	
Pulsed Drain Current	I_{DM}	± 20	± 15	A
Continuous Source Current (Diode Conduction) ^a	I_S	1.25	-1.25	
Maximum Power Dissipation ^a	P_D	1.0	0.64	W
		0.64	0.64	
Operating Junction and Storage Temperature Range	T_J, T_{Stg}	-55 to 150		°C

Thermal Resistance Ratings

Parameter	Symbol	N- or P-Channel	Unit
Maximum Junction-to-Ambient ^a	R_{thJA}	125	°C/W

Notes

a. Surface Mounted on FR4 Board, $t \leq 10$ sec.

Subsequent updates to this data sheet may be obtained via facsimile by calling Siliconix FaxBack, 1-408-970-5600. Please request FaxBack document #1804.

Specifications ($T_J = 25^\circ\text{C}$ Unless Otherwise Noted)

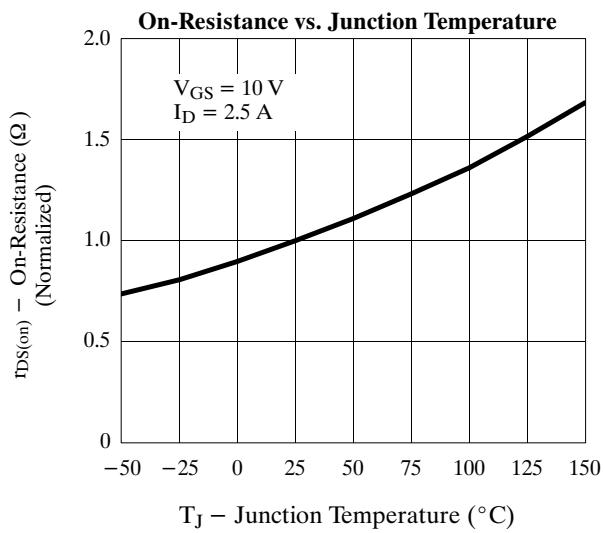
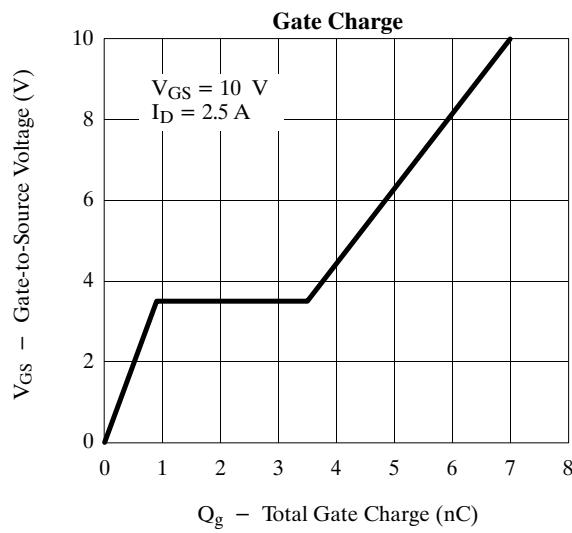
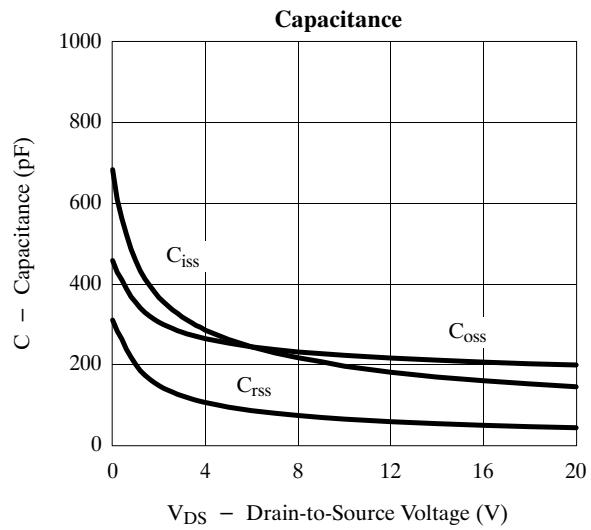
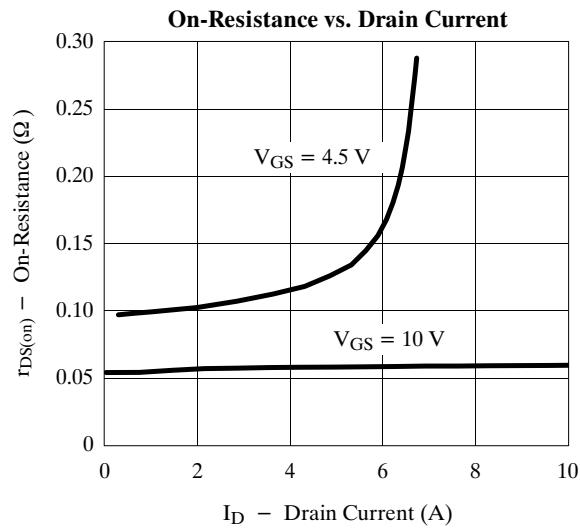
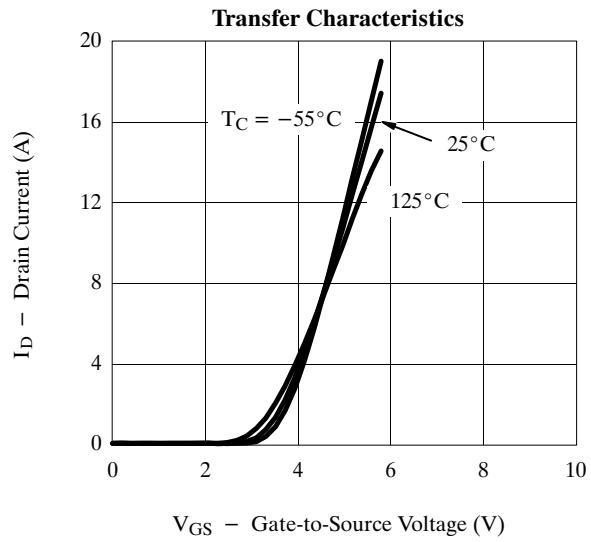
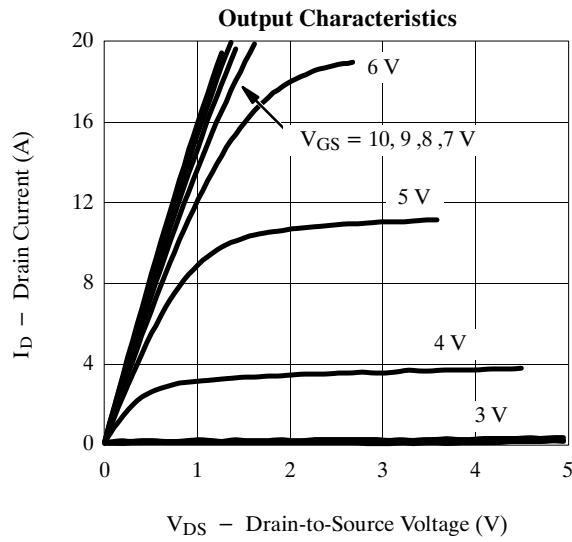
Parameter	Symbol	Test Condition		Min	Typ	Max	Unit
Static							
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	N-Ch	1.0			V
		$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	P-Ch	-1.0			
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$				± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$	N-Ch			1	μA
		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$	P-Ch			-1	
		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$	N-Ch			25	
		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^\circ\text{C}$	P-Ch			-25	
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	N-Ch	14			A
		$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	P-Ch	-10			
Drain-Source On-State Resistance ^a	$r_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}, I_D = 2.5 \text{ A}$	N-Ch		0.065	0.09	Ω
		$V_{GS} = -10 \text{ V}, I_D = 1.9 \text{ A}$	P-Ch		0.13	0.17	
		$V_{GS} = 4.5 \text{ V}, I_D = 1.8 \text{ A}$	N-Ch		0.100	0.175	
		$V_{GS} = -4.5 \text{ V}, I_D = 1.3 \text{ A}$	P-Ch		0.26	0.32	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15 \text{ V}, I_D = 2.5 \text{ A}$	N-Ch		5		S
		$V_{DS} = -15 \text{ V}, I_D = -1.9 \text{ A}$	P-Ch		3		
Diode Forward Voltage ^a	V_{SD}	$I_S = 1.25 \text{ A}, V_{GS} = 0 \text{ V}$	N-Ch		0.8	1.2	V
		$I_S = -1.25 \text{ A}, V_{GS} = 0 \text{ V}$	P-Ch		0.8	-1.2	
Dynamic^b							
Total Gate Charge	Q_g	N-Channel $V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 2.5 \text{ A}$ P-Channel $V_{DS} = -10 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -1.9 \text{ A}$	N-Ch		7	10	nC
Gate-Source Charge	Q_{gs}		P-Ch		7	10	
Gate-Drain Charge	Q_{gd}		N-Ch		0.9		
Gate-Drain Charge	Q_{gd}		P-Ch		1.3		
Turn-On Delay Time	$t_{d(\text{on})}$	N-Channel $V_{DD} = 10 \text{ V}, R_L = 10 \Omega$ $I_D \approx 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 6 \Omega$	N-Ch		2.1		ns
Rise Time	t_r		P-Ch		1.7		
Turn-Off Delay Time	$t_{d(\text{off})}$		N-Ch		11	20	
Fall Time	t_f		P-Ch		9	20	
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = 1.25 \text{ A}, di/dt = 100 \text{ A}/\mu\text{s}$	N-Ch		11	20	ns
		$I_F = -1.25 \text{ A}, di/dt = 100 \text{ A}/\mu\text{s}$	P-Ch		12	25	

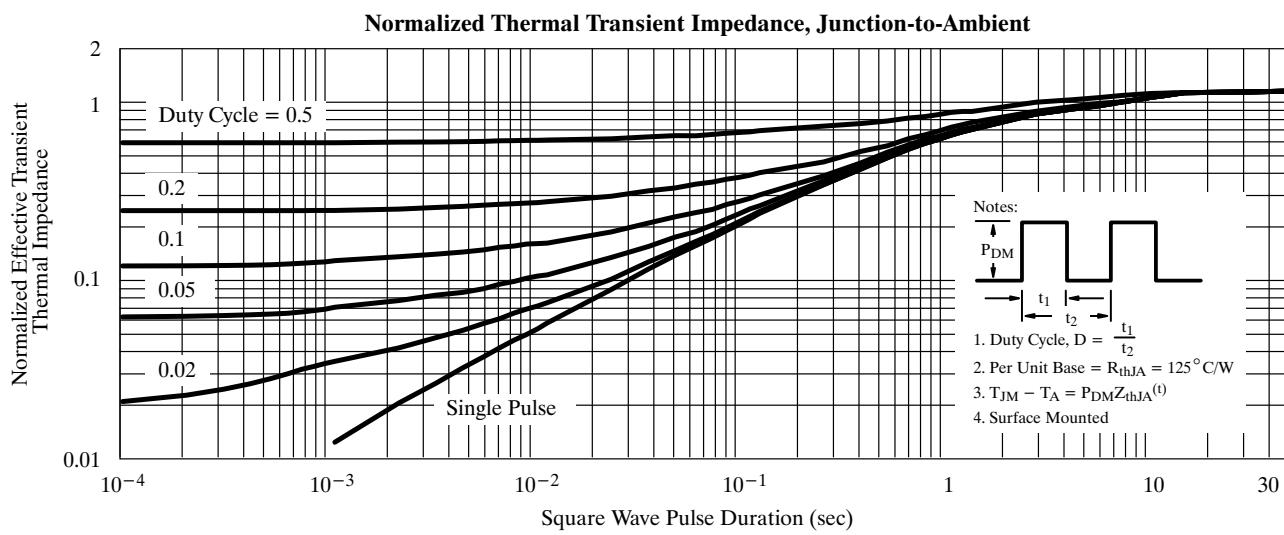
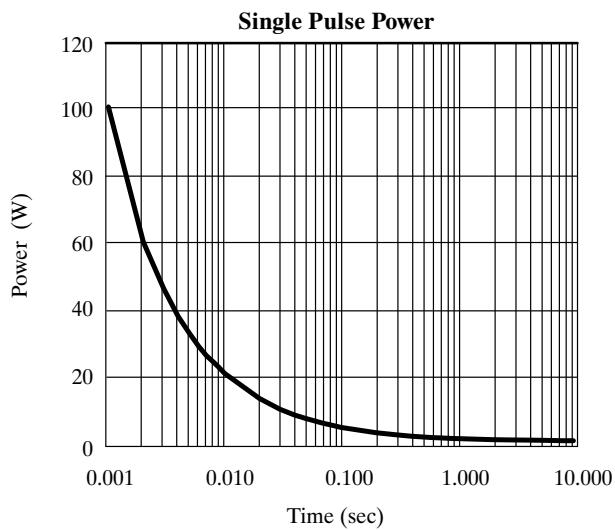
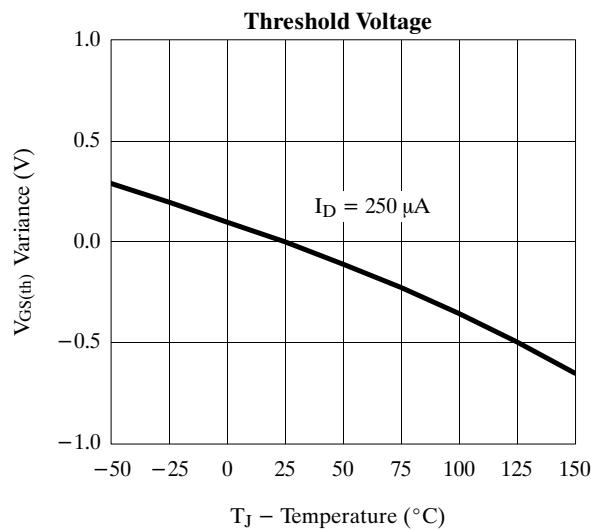
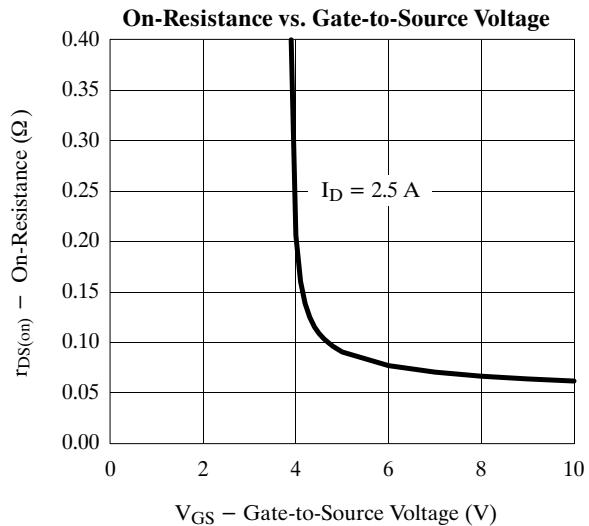
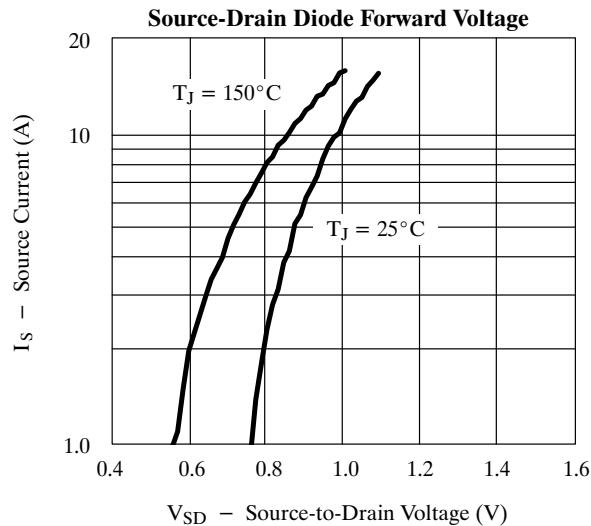
Notes

- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.

Typical Characteristics (25°C Unless Noted)

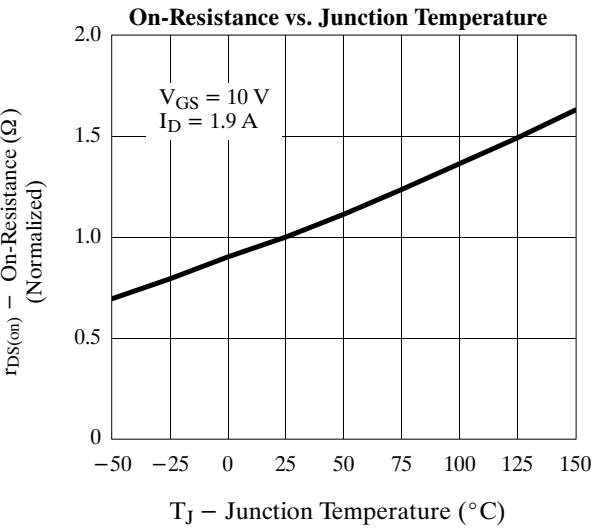
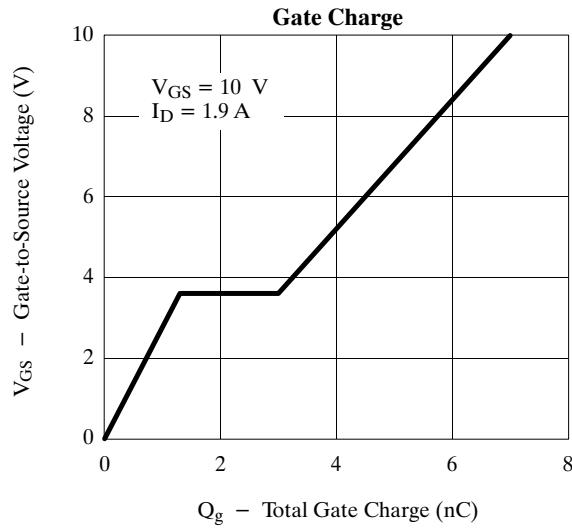
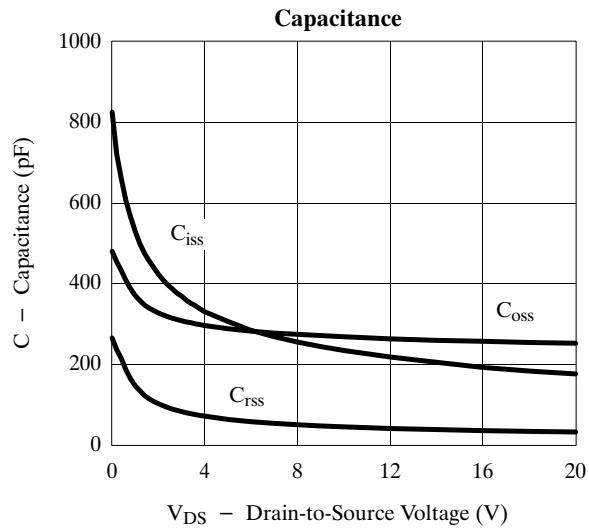
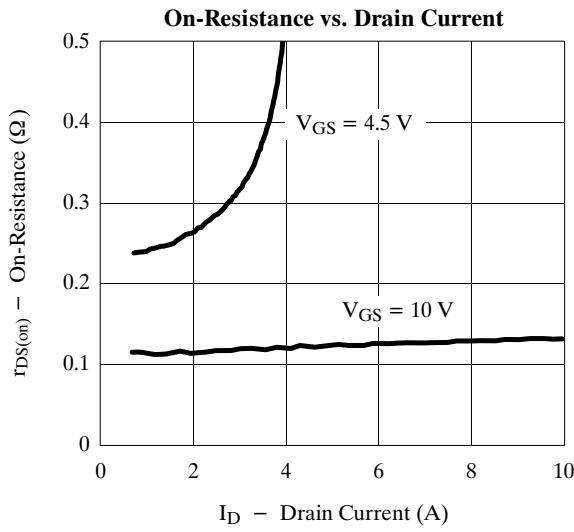
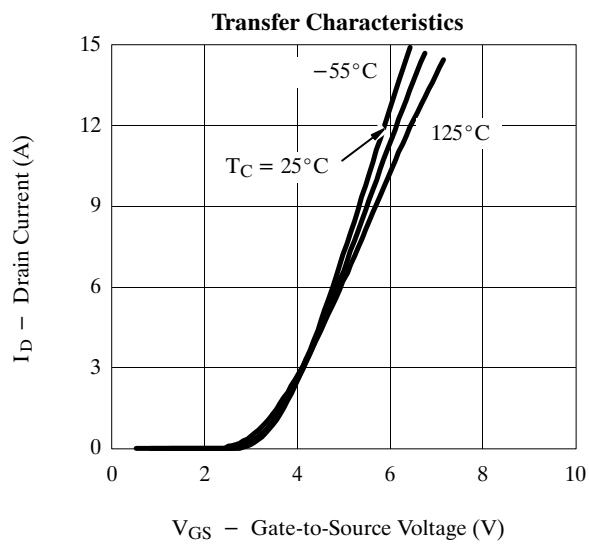
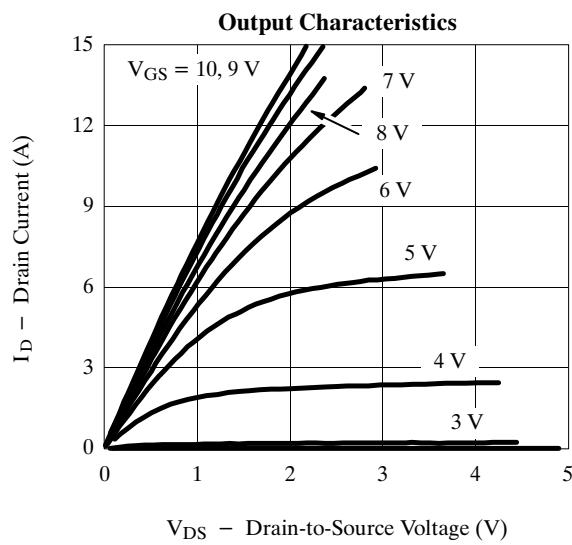
N-Channel



Si6542DQ**Typical Characteristics (25°C Unless Noted)****N-Channel**

Typical Characteristics (25°C Unless Noted)

P-Channel



Typical Characteristics (25°C Unless Noted)

P-Channel

